



TG332 Series CPU

FEATURES

- 20 MHz, 32 Bit 68332 Processor
- 68882, 32 Bit Floating Point Coprocessor Capability
- Up to 1 Mbyte of battery backed RAM and 1 Mbyte of EPROM, with provisions for future expansion of an additional 1 Mbyte of each
- Contains 5 EIA RS-232 Serial Ports: 2 can be converted to RS-422/485
- Supports multiple protocols to communicate with third-party equipment (DNP, Modbus, JEM, Schweitzer, etc.)
- Digital input scanning via hardware logic
- Interrupts when it detects a digital input change
- Watchdog Timer for qualification of digital outputs
- Interfaces to the Tetragenics' 2000 Series Bus
- Real-Time clock synchronized to a 60 Hz line frequency, with LED indication of synchronization
- Up to 38.4 kbaud data rate on serial ports
- Daughter card provision
- Hardware selectable ID
- LED indication for + 5 Vdc, Reset, Watchdog Timer Status, and Program Memory Access

ENVIRONMENTAL DATA

Operational Temperature: 0 to 60° C
 Storage Temperature: -40 to 70° C
 Humidity: 95% Noncondensing

MECHANICAL DATA

Standard Double-Height Eurocard; Over-all sizes to DIN 41 494 part 2, IEC 297-3 (6.299" x 9.187")

Front Panel: *6 U x 4 HP (10.3" x .8")
 Weight: Approximately 15 oz.

* U = 1 unit (approx. 1.71 inches)
 HP = Horizontal Pitch approx. 0.2 inches

POWER REQUIREMENTS

750 mA @ +5 VDC

DESCRIPTION

The Tetragenics TG332 Series Central Processing Board (CPU) is a microprocessor computer board that interfaces to the Tetragenics 2000 Series Bus. It enhances Supervisory Control and Data Acquisition (SCADA) systems, hydroelectric systems, and Communications Monitoring and Control Remote Terminal Units.

The board offers fast system speeds, multiple communications ports (that allow your system to talk to other-vendor equipment with differing protocols), increased memory (up to 1 Mbyte RAM and 1 Mbyte EPROM), and increased processing power (20 MHz, 32 bit processor and 32 bit floating point coprocessor capability).

The TG332 Series' modular design makes the board options field changeable. Other features include peer-to-peer multiplexing (multiple RTUs can exist at the same site using only one communications channel back to the Master) and a hardware selectable ID feature (this eliminates the need to set the station ID with a diagnostics terminal at the site).

